# Master of Science Computer Engineering (Technische Informatik) MScTI

Description of the course modules



## Heidelberg University Department of Physics and Astronomy

Version 1.15

Name of university	Heidelberg University
Name of department	Department of Physics and Astronomy
Name of degree course	Master of Science Computer Engineering
Format of studies	Full time, part time
Type of degree course	Consecutive
Date of version	24.06.2020
Prescribed period of study	Two years, i. e. four semesters
Location	Heidelberg
Total number of credit points	120
University places	Unlimited
Target group	Holders of Bachelor of Science, Magister, Staatsexamen, Diploma or equivalent final degree of at least 6 semester study having majored in computer science or in Mathematics, Natural Sciences or Engineering with attestable lectures (recommended 24 CP) imparting knowledge in computer science.

#### Preamble

#### Quality Objectives at Heidelberg University for Study Programmes and Teaching

Having regard to its mission statement and constitution, Heidelberg University's degree courses have subject-related, interdisciplinary and occupational objectives. They aim to provide a comprehensive academic education equipping graduates for the world of work.

Consequently, the following competence profile has been drawn up for inclusion in module handbooks as a profile of skills valid for all disciplines. It shall be applied to the specific objectives of the individual courses and then implemented in their curricula and modules.

The main points of the competence profile:

- developing subject-related skills with a pronounced research orientation
- developing the ability to engage in trans disciplinary dialogue
- developing practice-related problem-solving skills
- developing personal and social skills
- promoting the willingness to assume social responsibility on the basis of the skills acquired

#### Disciplinary and Interdisciplinary Quality Objectives of the Master Programme "Computer Engineering"

The research oriented master program in Computer Engineering (MScTI) at Heidelberg University is organized by the Institute of Computer Engineering and the Department of Physics and Astronomy. Its educational objective is to qualify students for a research or development oriented professional career in the field of Computer Engineering as well as for participation in PhD programs. The students should get a thorough understanding of possible approaches and solutions and should be able to assess their advantages and drawbacks so that they can chose a good solution for a given problem. They should realize when a solution is inappropriate or suboptimal and should be able to devise novel approaches / solutions. The MScTI emphasizes on practical skills so that the students can use the available tools and methods (software, mathematics) to develop working solutions efficiently.

Students can choose one of three specializations, which are sub-fields of 'computer engineering' ('Application Specific Computing', 'Microelectronics', 'Robotics, Haptics and Biomechanics'). Each specialization consists of a set of modules on an advanced level, which cover the field to a large extent. By following a sufficient number of modules in such a specialization, students reach the state-of-the art in the area to become fully competitive.

After having completed the research phase (seminar, student research project, master thesis), students have obtained the ability to do research independently, to document and publish research work. They deepen their knowledge on scientific methods, information engineering, hardware and software, interdisciplinary system thinking, experience in practical applications as well as the communication competence and the ability to work in teams.

Possible career options are in the practical development of hardware systems for data acquisition and for fast data processing, the efficient solution of compute-intensive tasks on modern, high performance, heterogeneous hardware, the design of analogue or digital microelectronics circuits, the design and operation of robotic systems, etc.

#### **Subject-related Qualification Objectives**

After completing the master program 'Computer Engineering' the graduates are able to program parallel systems with shared and distributed memory and use the learned structures to develop new architectures of parallel computers. They acquire a deep understanding of nonlinear dynamical systems in order to design simple nonlinear control systems. Additionally, they understand concepts about the functionality and programming of microprocessors and peripheral circuits as well as reconfigurable architectures and use these concepts to implement an own circuit respectively to implement and to program a sample embedded FPGA platform.

#### **Transdisciplinary Qualification Objectives**

Master's graduates in Computer Engineering possess the required skills to work independently with a variety of software tools for various special applications and to choose the appropriate one to solve problems. They are able to apply structured working methods and can organize complex professional projects. Also they have a basic understanding about legal aspects of founding and running a company, financial aspects of founding and running a business and are able to apply marketing strategies and tools.

## Overview of the course modules

Module	Module Coordinator	ECTS
Fundamentals		
<ul> <li>Parallel Computer Architecture (compulsory)</li> </ul>	Brüning	6
<ul> <li>Control Systems Design (compulsory, "System Design" in PO)</li> </ul>	Masia	6
■ C++ Practice	Strzodka	6
Electronics	Wurz	6
Introduction to High Performance Computing	Fröning	6
<ul> <li>Microcontroller Based Embedded Systems</li> </ul>	Wurz	6
Reconfigurable Embedded Systems	Kugel	6
Soft Skills		
• Tools	all	4
Entrepreneurship	extern	6
Main Subject / Specialization		
Components, Basic Circuits & Simulation	Fischer	6
Full Custom VLSI Design	Fischer	6
Digital Hardware Design	Brüning	6
<ul> <li>Digital Semi Custom Design Flow</li> </ul>	Brüning	6
Functional Verification	Brüning	6
Advanced Analogue Building Blocks	Fischer	6
<ul> <li>Silicon Sensors &amp; Readout Electronics</li> </ul>	Fischer	6
<ul> <li>GPU Computing</li> </ul>	Fröning	6
<ul> <li>Accelerator Practice</li> </ul>	Strzodka	6
Parallel Algorithm Design	Strzodka	6
Advanced Parallel Algorithms	Strzodka	6
Advanced Parallel Computing	Fröning	6
FPGA Coprocessors	Kugel	6
High Performance Interconnection Networks	Brüning	6
Robotics 1 - Kinematics, Dynamics and Control	Masia	6
Robotics 2 - Simulation and Optimization in Robotics	Masia	6
<ul> <li>Biomechanics and Biorobotics</li> </ul>	Masia	6
Haptics and Human Robot Interaction / Rehabilitation	Masia	6
<ul> <li>Robotics Practical</li> </ul>	Masia	6
<ul> <li>Robotic Games</li> </ul>	Masia	6

### **Structure of Courses**

All in all, the following modules have to be completed successfully (120 CP):

•	3 modules from "Fundamentals"	18 CP
•	5 modules from "Main Subject / Specialization"	30 CP
•	2 modules from "Free Courses"	12 CP
•	2 or more modules from "Soft Skills"	12 CP
•	Seminar	4 CP
•	Student research project	14 CP
•	Master thesis with final colloquium	30 CP

#### **Fundamentals**

The following modules from "Fundamentals" are compulsory and have to be completed (mandatory modules):

- Parallel Computer Architecture
- Control Systems Design

As an elective you can choose any other subject listed above in "Fundamentals".

#### Main Subject / Specialization

In principle, advanced students can choose their in-depth modules freely according to the examination rules / regulations. We recommend, however, following one of the model curricula completing an exceptional specialization in a certain field of Computer Engineering. When completing a sufficient number of modules in such a specialization during your studies, this specialization will be documented explicitly in your Master Grade Report.

This is a list of model curricula and the modules required in each case:

#### 1. Microelectronics

2 elective modules from:

3 compulsory modules: - Components, Basic Circuits & Simulation

- Digital Hardware Design
- Full Custom VLSI Design
- Advanced Analog Building Blocks
- Digital Semicustom Design Flow
- Electronics
- Functional Verification
- Microcontroller Based Embedded Systems
- Reconfigurable Embedded Systems
- Silicon Sensors & Readout Electronics

### 2. Application Specific Computing

3 compulsory modules:

2 elective modules from:

- GPU Computing
- Parallel Algorithm Design
- Reconfigurable Embedded Systems
- Accelerator Practice
- Advanced Parallel Algorithms
- Advanced Parallel Computing
- C++ Practice
- Electronics
- FPGA Coprocessors
- High Performance Interconnection Networks
- Introduction to High Performance Computing
- Microcontroller Based Embedded Systems

#### 3. Robotics, Haptics and Biomechanics

3 compulsory modules:	- Robotics 1 - Kinematics, Dynamics and Control
	- Biomechanics and Biorobotics
	- Haptics and Human Robot Interaction / Rehabilitation
2 elective modules from:	- Robotics 2 - Simulation and Optimization in Robotics
	- Robotics Practical
	- Robotic Games
	- GPU Computing
	- Microcontroller Based Embedded Systems
	- Reconfigurable Embedded Systems

#### **Free Courses**

The aim of the Free Course is to broaden ones expertise. The lectures can be chosen from the course catalogue of the Heidelberg University. To be approved as a Free Course in the MScTI, the lecture must meet the following three conditions:

- it is graded,
- the sum of credit points awarded is 12 CP or more,
- it reasonably contributes to a broadening of the expertise for this study program.

In case of doubt, the Dean of Studies makes the decision on the approval.

It is also possible to select Fundamentals or Specializations modules from the MScTI for the Free Course. When the decision on a preferred specialization is not yet made in the first semester, it is a good choice to take a second specialization as the Free Course.

## Soft Skills

All in all, 12 CP must be completed in the field of soft skills, 2 of which are integrated in the seminar. For the remaining 10 CP the following courses can be chosen:

- Tools (4 CP),
- Entrepreneurship (6 CP),
- Courses from the University course program classified as soft skill courses,
- Language Courses (6 CP maximum).

#### Seminar

For the seminar 2 CP are allocated as soft skills in addition to 4 CP for the professional contents. Seminar: 4 CP (professional contents) + 2 CP (soft skills)

## Fundamentals:

<b>Code:</b> MScTI_PCA		Course Title: Parallel Computer Arc	hitecture	
Module Coordina Prof. Dr. U. Brünin	ı <b>tor:</b> Ig	<b>Type:</b> Lecture with exercises	s / lab /	
<b>Credit Points:</b> 6	<b>Workload:</b> 180h	<b>Teaching Hours:</b> 4 / week	<b>Language:</b> German / English	Term: WS
<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture (2 hours/week)</li> <li>Exercise / Lab: practical programming exercises on parallel computer system (2 hours/week)</li> </ul>				em (2
<ul> <li>Objectives: The students</li> <li>understand the concepts and principles of parallel processing and the underlying hardware structures, so that they are able to program parallel systems with shared and distributed memory,</li> <li>use the learned structures to develop new architectures of parallel computers.</li> </ul>				erlying shared ers.
Contents: • Concepts of Parallel Processing • SIMD-Architectures • MIMD-Architectures • Shared Memory • Distributed Memory • Communication and Synchronization • Multithreading • Taxonomy of Interconnection Networks • Point-to-Point INs • Switched INs, Shuffles, Crossbars, Routing, Latency • Communication Protocols • Virtual Shared Memory • Dataflow Architectures				
Prerequisites: none		Recommended Know basic knowledge of Co	wledge: omputer Architecture	
<b>Literature:</b> A reading list will be provided in the script. The script will be accessible on the web site of the Computer Architecture Chair.				
<b>Form of Testing and Examination:</b> 30' oral exam at the end of the semester At least 50% of the exercises must be passed.				

<b>Code:</b> MScTI_CSD		Course Title: Control Systems Design			
Module Coordina Prof. Dr. L. Masia	tor:	<b>Type:</b> Lecture with exercises			
Credit Points:	Workload:	Teaching Hours: Language: Term:			
6	180h	4 / week	German / English	SS	
Module Parts and	I Teaching M	ethods:			
<ul> <li>Lecture: 2 hours</li> <li>Exercise with here</li> </ul>	s/week omework: 2 h	ours/week			
Objectives: After	completing th	is course the students wil	be able to:		
<ul> <li>describe linear</li> <li>analyze linear s method,</li> <li>design linear co</li> <li>apply the method</li> </ul>	<ul> <li>describe linear phenomena and linear dynamical systems,</li> <li>analyze linear systems by using state space representation, root locus and nyquist method,</li> <li>design linear control systems based on classical PID control scheme,</li> <li>apply the methods to simple practical examples in engineering and physics.</li> </ul>				
Contents:					
<ul> <li>Introduction to feedback control</li> <li>Modeling in the frequency and time domain</li> <li>Time response of dynamic systems</li> <li>Reduction of multiple subsystems</li> <li>Stability analysis</li> <li>Steady-state errors</li> <li>Root locus techniques</li> <li>Controller design via root locus</li> <li>Frequency response techniques</li> <li>Design via frequency response</li> <li>Design via state space</li> <li>Introduction to digital control systems</li> </ul>					
Prerequisites: none		Recommended Knowle Theory of linear systems	edge:		
<ul> <li>Literature:</li> <li>K. Ogata: "Modern Control Engineering"</li> <li>Gene F. Franklin, J. David Powell, e al.: "Feedback Control of Dynamic Systems"</li> <li>W. Bolton: "Bolton: Mechatronics"</li> <li>Basilio Bona: "Dynamic modelling of mechatronic systems"</li> </ul>					
Form of Testing and Examination: Written exam at the end of the semester. Successful participation in the programming exercises is required to be accepted to exam.					

Code:		Course Title:			
MScTI_ADVCPP		C++ Practice			
Module Coordina Prof. Dr. R. Strzod	Module Coordinator: Prof. Dr. R. Strzodka		<b>Type:</b> Lecture with exercises and project		
<b>Credit Points:</b> 6	<b>Workload:</b> 180h	Teaching Hours:Language:Term:4 / weekGerman / EnglishWS			
<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture 2 hours/week</li> <li>Exercise 1 hour/week on average plus homework</li> <li>Project 1 hour/week on average plus homework</li> </ul>					
<ul> <li>Objectives: Students are able to</li> <li>apply all major features of modern C++,</li> <li>design better programs following guidelines for an effective programming style,</li> <li>use and combine different programming patterns.</li> </ul>				style,	
<ul> <li>Contents:</li> <li>A tour of moder</li> <li>Practical use of initializer lists, la</li> <li>How to select a</li> <li>Clear and effect</li> <li>Intensive exerc</li> <li>This is an advance knowledge and ex</li> </ul>	<ul> <li>Contents:</li> <li>A tour of modern C++ from start to end</li> <li>Practical use of new functionality since C++11, e.g. constexpr, move refs and ctors, initializer lists, lambdas, variadic templates, variants, threads, regex, tuples, bindings</li> <li>How to select among the language features</li> <li>Clear and effective programming style</li> <li>Intensive exercises with practical applications of discussed C++ feature</li> <li>This is an advanced programming course, not suitable for C++ beginners! Without prior</li> </ul>				
Prerequisites: none	es: Recommended Knowledge: Understanding of all basic C++ concepts such as references, classes, inheritance, overloading, templates, STL Multi-year programming experience				
Literature:					
<ul> <li>Bjarne Stroustrup: "A Tour of C++", Addison-Wesley, 2014</li> <li>Bjarne Stroustrup: "The C++ programming language", 4th ed, Addison-Wesley, 2013</li> </ul>					
<b>Form of Testing and Examination:</b> 50% of points from the exercises are required for participation in the project exam, which consists of a software design, an oral					

presentation and a written report, including a statement of independent, unaided project work. Alternatively to the project exam, an oral (20 min) exam may be announced by the Module Coordinator.

Code: MScTI_ELEC		Course Title: Electronics			
Module Coordina A. Wurz	tor:	<b>Type:</b> Lecture with exercises			
<b>Credit Points:</b> 6	<b>Workload:</b> 180h	Teaching Hours:Language:Term:4 / weekGerman / EnglishWS			
<ul> <li>Module Parts and</li> <li>Lecture on "Election of the Exercise with here</li> </ul>	l <b>Teaching M</b> ctronics" (3ho omework (1 ho	ethods: urs/week) ours/week)			
<ul> <li>Objectives: After t</li> <li>analyze the app</li> <li>understand the</li> <li>design simple e</li> </ul>	this course the dication of act methods of ci lectronic circu	e students will be able to: ive and passive compone rcuit design, its and apply the method	ents, Is to practical examp	les.	
Contents: • Resistors, capacitors, inductivities • Diodes (rectifiers, switches) • Transistors (amplifier, switches) • Field-effect transistors (JFET, MOSFET) • Operational amplifier (amplifier, analog filter) • Oscillators (LC oscillators, crystal oscillators) • Phase-locked loop, Laplace transformation • Power supply circuits • Transmission of analog and digital signals • Analog to digital conversion • Simulation of circuits					
Prerequisites: none	Prerequisites:     Recommended Knowledge:       none     none				
<ul> <li>Literature:</li> <li>Horowitz and Hill: "The Art of Electronics"</li> <li>Herrmann Hinsch: "Elektronik"</li> <li>U. Tietze, Ch. Schenk: "Halbleiterschaltungstechnik"</li> </ul>					
Form of Testing a beginning of cours	n <b>d Examinat</b> e	ion: To be defined by M	odule Coordinator be	efore	

Code:		Course Title:		
MScTI_INTROHP	С	Introduction to High Performance Computing		g
Module Coordina Prof. Dr. H. Frönin	l <b>tor:</b> g	Type: Lecture with exercise		
Credit Points:	Workload:	Teaching Hours:	Language:	Term:
6	180h	4 / week	German / English	WS
Module Parts and • Lecture (2 hour • Exercise with he	arts and Teaching Methods: (2 hours/week) e with homework (2 hours/week)			
Objectives: Stude	ents			
<ul> <li>know message passing and scalable programming</li> <li>are familiar with the most important past and present concepts for large-scale computing problems</li> <li>can design and optimize solutions for large-scale computing problems</li> <li>know how to use MPI and related software tools to implement large-scale computing problems</li> <li>are capable to solve large-scale computing problems with objectives including performance in terms of time and energy, and scalability in terms of time and capacity, and capacity, and capacity and capacity.</li> </ul>				ale computing ling nd
<ul> <li>Contents:</li> <li>HPC architectures and message passing</li> <li>Parallel algorithm design and Message Passing Interface (MPI)</li> <li>MPI internals</li> <li>Workload characterization</li> <li>Short introduction to accelerated computing</li> <li>Practical problems and their solutions</li> </ul>				
Prerequisites: none	Prerequisites: noneRecommended Knowledge: Computer architecture basics, parallel programming principles, C, C++, OS basics			
<ul> <li>Literature:</li> <li>Georg Hager, Gerhard Wellein: "Introduction to High Performance Computing for Scientists and Engineers", Taylor &amp; Francis Inc</li> </ul>				iting for
Form of Testing and Examination: 15 – 30 min. oral exam or 1h written exam announced by Module Coordinator				

Code:		Course Title:				
MScTI_MES		Microcontroller Based Embedded Systems				
<b>Module Coordina</b> A. Wurz	itor:	<b>Type:</b> Lecture with exercises				
Credit Points:	Workload:	Teaching Hours:	Language:	Term:		
6	180h	4 / week German / English SS				
Module Parts and • Lecture (2h) • Exercise/ Lab v	<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture (2h)</li> <li>Exercise/ Lab work (2h)</li> </ul>					
Objectives: After	this course the	e students will be able to	D:			
<ul> <li>analyse active and passive electronic components for practical applications,</li> <li>understand the methods of circuit design, the functionality and programming of microprocessors and peripheral circuits so that they can build a complex microprocessor circuit themselves,</li> <li>install a development environment for program development,</li> <li>apply methods for debugging in microprocessor circuits.</li> </ul>				s, ig of		
Contents:						
<ul> <li>project manage</li> <li>circuit design</li> <li>microcontrollers</li> <li>mp3 decoder +</li> <li>power supply (I</li> <li>selection of cor</li> <li>CAD program (</li> <li>manufacturing I</li> <li>stuffing boards</li> <li>C program deve</li> <li>test program</li> <li>implementing +</li> <li>electronic cons</li> </ul>	<ul> <li>project management</li> <li>circuit design</li> <li>microcontrollers</li> <li>mp3 decoder + Ethernet</li> <li>power supply (linear regulators +switching regulators)</li> <li>selection of components</li> <li>CAD program (schematic +layout)</li> <li>manufacturing boards</li> <li>stuffing boards</li> <li>C program development</li> <li>test program</li> <li>implementing + debugging</li> <li>electronic construction techniques</li> </ul>					
Prerequisites:		Recommended Know	/ledge:			
<ul><li>Literature:</li><li>Paul Horowitz, Winfield Hill: "The Art of Electronics"</li></ul>						
Form of Testing a beginning of cours	and Examinat	tion: To be defined by N	Nodule Coordinator be	efore		

Code:		Course Title:		
MScTI_RES		Reconfigurable Embe	dded Systems	
Module Coordinator: Dr. A. Kugel		Type: Lecture with exercises		
Credit Points:	Workload:	Teaching Hours:	Language:	Term:
6	180h	4 / week	German / English	SS
Module Parts and	I Teaching M	ethods:		
<ul> <li>Lecture (2 hour homework</li> <li>Just-in-time tea</li> </ul>	s) / practical e ching session	exercise (lab, 1 hour ave s (4 hours) on selected	ı) / project (lab, 1 hour topics	avg) /
<ul> <li>Objectives: After completing this course students are able to</li> <li>list and explain important elements and properties of embedded systems,</li> <li>describe fundamental principles and components of reconfigurable technology,</li> <li>apply elementary application design methodologies for microprocessors and FPGAs to implement, program and test a sample embedded FPGA platform.</li> </ul>				ogy, d FPGAs
<ul> <li>Contents:</li> <li>Requirements and specific properties of embedded systems</li> <li>Overview on hardware components: microcontrollers, peripherals, FPGAs</li> <li>Real-time issues and scheduling</li> <li>FPGA design tools: HDL (incl. VHDL tutorial), simulator, debugger</li> <li>System-on-Chip architecture – controller, buses and peripherals</li> <li>HW/SW co-design</li> <li>Embedded system software (stand-alone and real-time kernels)</li> </ul>				
Prerequisites: none		Recommended Know none	vledge:	
<ul> <li>Literature:</li> <li>Peter Marwedel: "Eingebettete Systeme", Springer Lehrbuch, 1. Auflage 2007</li> <li>Th. Flick, H. Liebig: "Mikroprozessortechnik", Springer Lehrbuch, 4. Auflage 2004</li> <li>H. Bähring: "Mikrorechner-Systeme", Springer Lehrbuch, 3. Auflage 2002</li> <li>Karim Yaghmour: "Building Embedded Systems", O'Reilly, April 2003</li> </ul>				
Form of Testing and Examination: 50% score on exercises plus either oral exam (15min) or project (see below). Available option announced at start of course.				

**Project exam:** autonomous elaboration of project task assigned by Module Coordinator. Successful completion requires all of: operational design/program, written report (5 pages), presentation (10 minutes) with colloquium, statement of unaided work.

#### Soft Skills:

Code: MScTI_TOOLS		Course Title: Tools		
Module Coordin several, changing	ator:	Type: Lecture with exercises		
<b>Credit Points:</b> 4	Workload: 120h	<b>Teaching Hours:</b> 4 / week	<b>Language</b> German / English	Term: WS
Module Parts an • Lecture (2 h / · • Supervised Ex	d <b>Teaching M</b> week) xercises (2 h / w	ethods: veek)	L	L
<ul> <li>Objectives: Stud</li> <li>have an overving frequent tasks mathematical working in a term</li> <li>are able to imp</li> <li>are aware that</li> <li>are able to demintroductions generations</li> </ul>	<ul> <li>Objectives: Students</li> <li>have an overview of the functionalities of various software tools suited to accomplish frequent tasks, like the creation of drawings and illustrations, programming, solving of mathematical problems, analysis and visualization of data, search for literature or working in a team.</li> <li>are able to improve their work flows by choosing an appropriate tool</li> <li>are aware that application of a suited tool improves their working quality and efficiency</li> <li>are able to deepen their knowledge and skill in the presented tools on the basis of the</li> </ul>			
Contents: Subjects are chosen from a pool of possibilities as a function of interests and need of students. The list is regularly adapted to new developments • introduction to Linux • version control tools (git, svn,) • introduction to python • mathematical software (Mathematica) • data evaluation and plotting (gnuplot, root) • software documentation tools (doxygen) • 2D & 3D drawing, construction and visualization (PovRay, OpnSCAD, PostScript, pdf) • styles and templates (powerpoint, word) • introduction to Latex • team work • project planning • literature search				
Prerequisites: none		Recommended Know none	wledge:	
Literature: anno	Literature: announced by Module Coordinator			
Form of Testing and Examination: Regular participation (maximum 2 courses missed without justification) and successful completion of supervised exercises				

Code: MScTI_ES		Course Title: Entrepreneurship		
Module Coordin tbd (external)	nator:	<b>Type:</b> Lecture with exercises	6	
<b>Credit Points:</b> 6	Workload: 180h	<b>Teaching Hours:</b> 4 / week	<b>Language:</b> German / English	Term: SS
Module Parts a <ul> <li>Lecture (2 ho)</li> <li>Exercise (2 h)</li> </ul>	nd Teaching M urs/week) ours/week)	ethods:		1
<ul> <li>Objectives: After completing this course students will be able to:</li> <li>use innovation methods like Design Thinking, Rapid Prototyping and Business Model Innovation to develop tangible solutions for real life problems</li> <li>present business cases in short presentations (pitches) to stakeholders and investors</li> <li>develop basic marketing strategies to find first customers for their products</li> <li>apply the legal framework to found their own company.</li> </ul>			iness Model and investors ts	
Contents: Design Think Rapid Prototy Business Mod Presentation finance marketing patent law an legal forms of	ing /ping del Innovation of business case d copyright f companies	es		
Prerequisites: none		Recommended Know none	wledge:	
<ul> <li>Literature:</li> <li>Eric Ries: "Lean Startup", Redline Verlag, 2012</li> <li>Alexander Osterwalder: "Business Model Generation", Campus Verlag, 2011</li> <li>Steve Blanck: "The Startup Owners's Manual", K &amp; S Ranch, 2012</li> <li>Ash Maurya: "Running Lean", O'Reilly, 2013</li> </ul>				
	_			

Main Subject / Specialization:

Code:		Course Title:		
MScTI_ANASIM		Components, Basic C	ircuits & Simulation	
Module Coordinat Prof. Dr. P. Fischer	t <b>or:</b>	Type: Lecture with exercises	3	
<b>Credit Points:</b> 6	<b>Workload:</b> 180h	Teaching Hours:Language:Term:4 / weekGerman / EnglishSS		
<ul><li>Module Parts and</li><li>Lecture (2h)</li></ul>	Teaching Me	ethods:		
Practical exercis	se with home	work (2h)		
Objectives: Stude	ents…			
<ul> <li>can use analogu</li> <li>know what an op</li> <li>can relate the gesignal properties</li> </ul>	peration point eometry and o	to analyze circuits in th is, how it affects circuit operation point of transi	e time and frequency of t behavior and how it c istors to their small- an	domain an be set d large
<ul> <li>Diode and transi</li> <li>Modelling of Dio</li> <li>Voltage and curri</li> <li>Component and</li> <li>Bode plot, transf</li> <li>Analogue simula</li> <li>Basic circuits: cu</li> <li>Practical exercise</li> </ul>	istor operation ode und MOS, rent sources, l circuit descri fer function ation (dc, ac, to urrent mirror, ses with profe	n principle , large / small signal mo Thevenin equivalent ption with complex vari transient) gain stage, cascode, so ssional simulation tools	odels ables ource follower, differen	tial pair
Prerequisites: none		Recommended Know	wledge:	

beginning of course

Code: MScTI_ANADESIGN		Course Title: Full Custom VLSI Design		
Module Coordinator: Prof. Dr. P. Fischer		<b>Type:</b> Lecture with exercises		
<b>Credit Points:</b> 6	<b>Workload:</b> 180h	<b>Teaching Hours:</b> 4 / week	Language: German / English	Term: WS
<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture (2h)</li> <li>Practical exercise with homework (2h)</li> </ul>				
<ul> <li>can carry out the complete design process from a circuit idea to a final, checked layout,</li> <li>understand how design rules are related to semiconductor properties or manufacturing issues,</li> <li>are able to practically carry out a mixed mode simulation,</li> <li>are able to extract parasitic values and perform a simulation with these parasitics,</li> <li>can program simple automatized scripts using SKILL</li> </ul>				
Contents: • Semiconductor manufacturing • Technology & design rules, technology files • Layout of components, rules, matching • Design Rule Check • Extraction, Layout versus Schematic Check • ESD and Antenna rules, latchup • Test equipment & test procedures • Script programming using SKILL • Parasitic extraction & simulation • Mixed Mode simulation				
Prerequisites: none	Prerequisites:         Recommended Knowledge:           none         MScTI_ANASIM			
Literature:     Lecture script available online				
Form of Testing a a simple circuit wit	and Examinat	tion: Design (schematic entation.	entry, simulation and	layout) of

Code:	Code: Course Title:			
MScTI_DIGHD		Digital Hardware Desig	n	
Module Coordinator: Prof. Dr. U. BrüningType: Lecture with exercises / lab /				
Credit Points:	Workload:	Teaching Hours:	Language:	Term:
6	180h	4 / week	German / English	WS
<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture (2 hours/week)</li> <li>Exercise: design and simulation of digital hardware with EDA software (2 hours/week)</li> </ul>				ours/week)
<ul> <li>Objectives: The s</li> <li>understand th design and ve able to use the can simulate a</li> </ul>	<ul> <li>Objectives: The students</li> <li>understand the concepts and principles of hardware design and the methodology for design and verification of hardware structures that means concretely that they are able to use their acquired knowledge to design new and efficient hardware and they can simulate and verify the developed designs.</li> </ul>			
<ul> <li>Contents:</li> <li>Introduction to the principles of hardware design</li> <li>use of Hardware Description Languages like Verilog HDL.</li> <li>design of combinational and sequential logic.</li> <li>overall design flow for Integrated Circuits</li> <li>Design descriptions</li> <li>Design elements</li> <li>Simulation</li> <li>Verification of Hardware</li> </ul>				
Prerequisites: none	Prerequisites:         Recommended Knowledge:           none         basic knowledge of Digital Circuit Design			
Literature: A reading list will be provided in the script. The script will be accessible on the web site of the Computer Architecture Chair.				
Form of Testing a At least 50% of the	and Examinat e exercises mi	t <b>ion:</b> 30' oral exam at the ust be passed.	e end of the semester	-

		Course Title: Digital Semi Custom Design Flow		
			Solgh T IOW	
Module Coordina Prof. Dr. U. Brünin	<b>tor:</b> g	<b>Type:</b> Lecture with exercises <i>i</i>	/ lab / project …	
Credit Points:	Workload:	Teaching Hours:	Language:	Term:
6	180h	4 / week	German / English	SS
Module Parts and	I Teaching M	ching Methods:		
<ul> <li>Lecture (2 hours/week)</li> <li>Exercise / Project: backend processing for ASICs with EDA software (2 hours/week)</li> </ul>				
Objectives: The s	tudents			
<ul> <li>deepen their knowledge of the methodology for semi-custom ASIC design,</li> <li>are able to use their acquired knowledge to design very complex chips,</li> </ul>				
can full the complete backend design process for modern chip technology.				
<ul> <li>Contents:</li> <li>Advanced methods for design of application specific ICs</li> <li>Synthesis of complex hardware systems</li> <li>Static Timing Analysis (STA)</li> <li>Place&amp;Route of modules and standard cells</li> <li>Signal integrity analysis</li> <li>Design rule checks</li> <li>Generation of mask data</li> <li>The SEED-2002 agreement between Cadence Design Systems and the University of Heidelberg allows our students to work and learn with the most modern EDA tools</li> </ul>				niversity of 0A tools
that are usually	only used in a	chip industry.		
Prerequisites: none		Recommended Knowl deeper knowledge of D	l <b>edge:</b> igital Hardware Desiç	gn
<b>Literature:</b> A reading list will be provided in the script. The script will be accessible on the web site of the Computer Architecture Chair.				
Form of Testing a	and Examinat	tion: 30' oral exam at the	e end of the semester	
At least 50% of the exercises and the chip project must be passed.				

<b>Code:</b> MScTI_DIGVERI		Course Title: Functional Verification		
Module Coordina Prof. Dr. U. Brünin	tor: g	<b>Type:</b> Lecture with exercises	/ lab /	
<b>Credit Points:</b> 6	Workload: 180h	Teaching Hours:Language:Term:4 / weekGerman / EnglishSS		
<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture (2 hours/week)</li> <li>Exercise (2 hours/week)</li> </ul>				
<ul> <li>Objectives: The students</li> <li>understand the concepts and principles of functional verification and the methodology,</li> <li>use the acquired for building verification environments,</li> <li>are able to verify complex hardware designs.</li> </ul>				
Contents: <ul> <li>Introduction to the principles of functional verification</li> <li>Simulation-Based Verification</li> <li>Formal Verification</li> <li>Use of Hardware Verification Languages like System Verilog</li> <li>Use of Verification Methodologies like OVM</li> <li>Verification Planning</li> <li>Coverage Models</li> <li>Assortion-Based Verification</li> </ul>				
Prerequisites: none	Prerequisites: noneRecommended Knowledge: Experience in Digital Hardware Design			
<b>Literature:</b> A reading list will be provided in the script. The script will be accessible on the web site of the Computer Architecture Chair.				
Form of Testing and Examination: 30' oral exam at the end of the semester At least 50% of the exercises must be passed.				

Code:		Course Title:		
MScTI_ANABLOCKS		Advanced Analogue Building Blocks		
Module Coordinator: Prof. Dr. P. Fischer		<b>Type:</b> Lecture with exercises		
Credit Points:	Workload:	Teaching Hours:	Language:	Term:
6	180h	4 / week	German / English	WS/SS
Module Parts and	Teaching M	ethods:		
<ul><li>Lecture (2 hour</li><li>Practical exerci</li></ul>	s/week) se (2 hours/we	eek)		
Objectives: The s	tudents			
<ul> <li>have a broad overview of advanced circuits so that they are able to chose an appropriate approach for a given problem</li> <li>get a deeper qualitative understanding of the behavior of analogue circuits,</li> <li>can quantitatively analyze analogue circuits and extract important figures of merit,</li> <li>know a large variety of advanced circuit topologies.</li> </ul>				
Contents:				
The lecture introduces various building blocks, mathematical tools or knowledge on more advanced topics, picked from the list below as a function of student background and interest. Content in SW or SS can vary.				
<ul> <li>Advanced transistor properties</li> <li>Feedback: properties, mathematical treatment, stability, Nyquist test</li> <li>Noise of components and circuits</li> <li>Transfer function, impulse response, poles and zeros</li> <li>Cascaded amplifiers</li> <li>Advanced current mirrors</li> </ul>				
<ul> <li>Differential circl</li> <li>DACs and ADC</li> </ul>	uits, common i S	mode feedback		
<ul> <li>Switches</li> </ul>				
<ul> <li>Switched Capa</li> </ul>	citor Circuits	ſ		
Prerequisites: none		Recommended Knowl MScTI_ANADESIGN	edge:	
Literature:				
<ul><li>Razavi: "Desigr</li><li>J. Millman: "Mic</li></ul>	n of analog CM proelectronics"	IOS integrated circuits"		
Form of Testing a beginning of cours	and Examinat	ion: To be defined by Mo	odule Coordinator be	fore

Code:		Course litle:		
MScTI_DET		Silicon Detectors & Rea	adout Electronics	
Module Coordina Prof. Dr. P. Fische	tor: r	<b>Type:</b> Lecture with exercises		
Credit Points:	Workload:	Teaching Hours:	Language:	Term:
6	180h	4 / week	German / English	WS
Module Parts and	Teaching M	ethods:		•
<ul><li>Lecture (2 hour</li><li>Exercise with here</li></ul>	s/week) omework (2 h	ours/week)		
Objectives: The s	tudents			
• know the basic	working princ	iples of silicon detectors,	so that they are able	to derive
important properties (speed, resolution)				
<ul> <li>know different sensor types with their properties, so that they can choose the best detector type for a given application</li> </ul>				
<ul> <li>know the basics on how to read out the signals and which figures of merit are of</li> </ul>				
importance so that they can design / chose readout concepts for a given application				
Contents:				
Basics				
o Inte	eractions of pa	rticles and photons with	matter (short)	
	niconductors, atial resolution	energy resolution noise	turing technology	
Particle Sensor	S			
○ PiN	Diodes, Pade	s, Pixel, Strips		
	PFETS, MAPS	ials		
Photo Sensors	-silcon mater	1015		
o Qua	antum efficien	cy, spectral sensitivity, re	esponse time	
○ API	Ds, SiPMs, C0	CDs, CMOS APS, others		
	s arge amplifier	Transimpedance amplifi	er bandwidth noise	
o Rea	adout chips fo	r strip- and pixel detector	S	
Applications				
Prerequisites:		Recommended Know	ledge:	
none		Basic knowledge in Ele	ctrodynamics, Quant	um
Litoroturo		wechanics and Solid S	iale Physics	
	nioonductor	OVIDOD" MULOU ICON 047	107/0/0	
<ul> <li>S. W. Sze: Ser</li> <li>G. Lutz: "Semic</li> </ul>	niconductor D	iation Detectors" Spring	1874248 or ISBN 3540648593	2
Rossi/Fischer/F	Rohe/Wermes:	"Pixel Detectors", Spring	ger, ISBN 354028332	23
Form of Testing a	and Examinat	tion: To be defined by M	- odule Coordinator be	fore
beginning of cours	e			

Code:		Course Title:		
MScTI_GPU		GPU Computing		
Module Coordinator: Prof. Dr. H. Fröning		<b>Type:</b> Lecture with exercises		
Credit Points:	Workload:	Teaching Hours:	Language:	Term:
6	180h	4 / week	German / English	WS
Module Parts and	I Teaching M	ethods:		·
<ul> <li>Lecture (2 hours/week)</li> <li>Exercise with homework (2 hours/week)</li> </ul>				
Objectives: Stude	ents			
<ul> <li>know the factors that determine the performance of GPU programs, and are able to program GPUs to solve computing problems,</li> <li>are familiar with GPU architecture and design decisions,</li> <li>can design and optimize CUDA programs for compute- or memory-intensive problems,</li> <li>know how to use CUDA tools to aid in programming, debugging and performance tuning,</li> <li>are capable to solve compute- or memory-intensive problems using GPUs with objectives including performance in terms of time and energy, and are capable to decide when accelerators like GPUs are suitable for a given computing problem.</li> </ul>				re able to ve rmance s with pable to oblem.
Contents:				
<ul> <li>Basics of GPU architecture and programming model</li> <li>Introduction to CUDA</li> <li>Performance optimization techniques</li> <li>Consistency and coherence of GPUs</li> </ul>				
Alternatives to				
none		Parallel programming, C	<b>eage:</b> C++ programming ski	lls
Literature:				
<ul> <li>T.G. Mattson, B.A. Sanders, B.L. Massingill: "Parallel Patterns for Parallel Programming", Addison Wesley 2004</li> <li>D.B. Kirk, W.W. Hwu: "Programming Massively Parallel Processors", Morgan-Kaufmann 2010</li> </ul>				
Form of Testing a	and Examinat	ti <b>on:</b> 15 – 30 min. oral ex	am or 1h written exa	m,

announced by Module Coordinator

<b>Code:</b> MScTI_PAD		Course Title: Parallel Algorithm Design		
Module Coordina Prof. Dr. R. Strzod	<b>tor:</b> ka	<b>Type:</b> Lecture with exercises a	and project	
<b>Credit Points:</b> 6	<b>Workload:</b> 180h	<b>Teaching Hours:</b> 4 / week	Language: German / English	Term: WS
<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture 2 hours/week</li> <li>Exercise 1 hour/week on average plus homework</li> <li>Project 1 hour/week on average plus homework</li> </ul>				
<ul> <li>Objectives: Students are able to</li> <li>exploit the available parallelism in modern CPUs,</li> <li>make design decisions depending on tradeoffs in parallel algorithms,</li> <li>apply and combine parallel patterns in their own programs.</li> </ul>				
Contents: Multiple levels of Parallel design Parallel data ac Communication Latency vs. thro Work efficiency Locality vs. para Tools for paralle Intensive exercise	of parallelism patterns ccess vs. computat oughput vs. step effici allelism el programmir ises	ion ency Ig		
Prerequisites: none		Recommended Knowl Basic C++	edge:	
<ul> <li>Literature:</li> <li>Michael McCool, Arch Robison, James Reinders: "Structured Parallel Programming", Morgan Kaufmann, 2012</li> </ul>				
<b>Form of Testing and Examination:</b> 50% of points from the exercises are required for participation in the project exam, which consists of a software design, an oral presentation and a written report, including a statement of independent, unaided project work. Alternatively to the project exam, an oral (20 min) exam may be announced by the Module Coordinator.				

Code:		Course Title:			
MScTI_ACC		Accelerator Practice			
Module Coordinator: Prof. Dr. R. Strzodka		<b>Type:</b> Lecture with exercises a	Type: Lecture with exercises and project		
Credit Points:	Workload:	Teaching Hours:	Language:	Term:	
6	180h	4 / week	German / English	SS	
Module Parts and	Teaching M	ethods:			
<ul> <li>Lecture 2 hours</li> <li>Exercise 1 hours</li> <li>Project 1 hour/</li> </ul>	s/week r/week on ave week on avera	rage plus homework ige plus homework			
<ul> <li>Objectives: Stude</li> <li>program accele</li> <li>create multi-bac</li> <li>select efficient program</li> </ul>	ents are able t rators on a hig ckend progran parallel algorit	to … gh-level with parallel patte ns that can run on differe hms from existing accele	erns, nt architectures, rator libraries.		
Contents:					
Overview of pro	ogramming pa	radigms for accelerators			
<ul> <li>Effective use of</li> </ul>	STL-like algo	rithm libraries			
<ul> <li>Multi-backend p</li> <li>Libraries for dot</li> </ul>	programming f	or different architectures			
<ul> <li>Specialized libration</li> </ul>	aries	e inteal aigebra			
Simultaneous u	ise of multiple	accelerators			
Prerequisites: none		Recommended Knowl Basic C++	edge:		
Literature:					
Will be annound	ced by the Mo	dule Coordinator			
Form of Testing a	and Examinat	tion: 50% of points from	the exercises are rec	quired for	
participation in the	project exam	, which consists of a SOTI	ware design, an oral		

participation in the project exam, which consists of a software design, an oral presentation and a written report, including a statement of independent, unaided project work. Alternatively to the project exam, an oral (20 min) exam may be announced by the Module Coordinator.

<b>Code:</b> MScTI_APA		Course Title: Advanced Parallel Algorithms			
Module Coordina Prof. Dr. R. Strzod	<b>tor:</b> ka	<b>Type:</b> Lecture with exercises a	Type: Lecture with exercises and project		
<b>Credit Points:</b> 6	Workload: 180h	<b>Teaching Hours:</b> 4 / week	Language: German / English	Term: WS	
<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture 2 hours/week</li> <li>Exercise 1 hour/week on average plus homework</li> <li>Project 1 hour/week on average plus homework</li> </ul>					
<ul> <li>Objectives: Students are able to</li> <li>apply advanced transformations to improve parallelism and locality,</li> <li>make detailed design decisions depending on tradeoffs in parallel algorithms,</li> <li>balance numerical efficiency and parallel efficiency.</li> </ul>					
<ul> <li>Contents:</li> <li>The lectures MScTI_PAD and MScTI_APA can be attended in the same semester in parallel. MScTI_PAD looks at more topics in breadth, while MScTI_APA looks at fewer topics in depth.</li> <li>Most recent developments in GPUs</li> <li>On-the-fly data transformations</li> <li>Data locality optimizations</li> <li>Hierarchical algorithms</li> <li>SIMD utilization</li> <li>Precision, accuracy and numerical schemes</li> <li>Numerical efficiency vs. parallel efficiency</li> <li>Deta representation</li> </ul>					
Prerequisites: none		Recommended Knowl Basic C++, C++11, CUI	<b>edge:</b> DA (e.g. MScTI_GPU	J)	
<ul> <li>Literature:</li> <li>David B. Kirk, Wen-mei W. Hwu: "Programming Massively Parallel Processors", 3rd ed, Morgan Kaufmann, 2017</li> <li>More will be announced by the Module Coordinator</li> </ul>					
<b>Form of Testing and Examination:</b> 50% of points from the exercises are required for participation in the project exam, which consists of a software design, an oral presentation and a written report, including a statement of independent, unaided project					

presentation and a written report, including a statement of independent, unaided project work. Alternatively to the project exam, an oral (20 min) exam may be announced by the Module Coordinator.

		<b>• •</b>			
Code:	Course litie:				
MScTI_APC		Advanced Parallel Com	puting		
Module Coordina Prof. Dr. H. Frönin	i <b>tor:</b> ig	<b>Type:</b> Lecture with exercises			
Credit Points:	Workload:	Teaching Hours:	Language:	Term:	
6	180h	4 / week	German / English	SS	
			<u> </u>		
<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture (2 hours/week)</li> <li>Exercise with homework (2 hours/week)</li> </ul>					
Objectives: The students					
<ul> <li>know principles coherence,</li> <li>are familiar with and multi-thread</li> <li>know how to de synchronization</li> </ul>	of parallel arc advanced co ding, ssign and optir problems,	chitectures, including syn ncepts like transactional nize complex parallel coo	chronization, consiste memory, relaxed cor de for particular comp	ency, and nsistency, pute and	
are capable of s	solving comple	ex computing problems u	sing massively parall	el	
processors, uno	Jerstanding th	e implications of architec	tural design decision	s on tv of	
certain process	or architecture	es for a given computing	problem.	ty Of	
Contents:					
<ul> <li>Principles of pa</li> <li>Shared memory</li> <li>Programming p algorithms</li> <li>Consistency mo</li> <li>Multi-/many-cor</li> <li>Emerging topics</li> </ul>	<ul> <li>Principles of parallel computing</li> <li>Shared memory architectures</li> <li>Programming paradigms, communication and synchronization concepts and algorithms</li> <li>Consistency models and scalable cache coherence</li> <li>Multi-/many-core and multi-threading architectures</li> <li>Emerging topics in parallel computing</li> </ul>				
Prerequisites: none		Recommended Know MScTI_PCA, MScTI_IN	ledge: ITROHPC, C++, OS	basics	
Literature:					
<ul> <li>John L. Hennessy, David A. Patterson: "Computer Architecture: A Quantitative Approach" (The Morgan Kaufmann Series in Computer Architecture and Design)</li> <li>Maurice Herlihy, Nir Shavit, "The Art of Multiprocessor Programming", Morgan Kaufmann</li> </ul>					
Form of Testing a announced by Mod	and Examinat dule Coordina	t <b>ion:</b> 15 – 30 min. oral ex tor	am or 1h written exa	m,	

<b>Code:</b> MScTI_FPGA		Course Title: FPGA Coprocessors		
Module Coordinator: Dr. A. Kugel		<b>Type:</b> Lecture with exercises		
Credit Points:	Workload:	Teaching Hours:	Language:	Term:
6	180h	4 / week	German / English	WS
Module Parts and	Teaching M	ethods:		
<ul> <li>Lecture (2 hours) / practical exercise (lab, 1-hour avg) / project (lab, 1-hour avg) / homework</li> <li>Just-in-time teaching sessions (4 hours) on selected topics</li> </ul>				
<b>Objectives:</b> After completing this course students are able to				
<ul> <li>list and explain advanced components of FPGA devices,</li> <li>list and explain coprocessor architectures and communication types,</li> <li>select, configure and program FPGA IP library elements,</li> <li>create custom IP cores using structural data-flow and FSM based control-flow design techniques,</li> <li>use IP cores to create hybrid applications for processor and reconfigurable coprocessor with appropriate interface mechanisms,</li> </ul>				flow design e
Contents:				
<ul> <li>Contents:</li> <li>Reconfigurable Computing Hardware <ul> <li>FPGA Device Architecture and Features</li> <li>Reconfigurable Computing Architectures</li> <li>(Re-)Configuration Management</li> </ul> </li> <li>Programming Reconfigurable Systems <ul> <li>Compute Models and System Architectures</li> <li>Programming FPGA Applications in VHDL</li> <li>Data- and Control- Flow Graphs</li> <li>High-Level Synthesis Tools</li> </ul> </li> <li>Mapping Designs to Reconfigurable Platforms <ul> <li>Technology Mapping</li> <li>Datapath Optimizations</li> </ul> </li> <li>Projects: Implementing Applications with FPGAs</li> </ul>				
Prerequisites: none		Recommended Know FPGA and HDL fundam	l <b>edge:</b> nentals (e.g. from MS	cTI_RES)
<ul> <li>Literature:</li> <li>Scott Hauck &amp; André Dehon: "Reconfigurable Computing", Morgan Kaufmann, 2008</li> </ul>				
Form of Testing a (15min) or project Project exam: aut	and Examinat (see below). <i>A</i> tonomous elab	tion: 50% score on exerc Available option announce poration of project task as	tises plus either oral ed at start of course. ssigned by Module C	exam oordinator.

**Project exam:** autonomous elaboration of project task assigned by Module Coordinator. Successful completion requires all of: operational design/program, written report (5 pages), presentation (10 minutes) with colloquium, statement of unaided work.

<b>Code:</b> MScTI_HPNET		Course Title: High Performance Interconnection Networks		
Module Coordinator: Prof. Dr. U. BrüningType: Lecture with exercises				
<b>Credit Points:</b> 6	Workload: 180h	<b>Teaching Hours:</b> 4 / week	Language: German / English	Term: SS
<ul> <li>Module Parts and</li> <li>Lecture (2 hours)</li> <li>Exercise with here</li> </ul>	<b>l Teaching M</b> s/week) omework (2 he	ethods: ours/week)		
<ul> <li>understand the</li> <li>will be able to c</li> <li>can use the lea networks.</li> </ul>	<ul> <li>Objectives: The students</li> <li>understand the concepts and principles of interconnection networks,</li> <li>will be able to configure and use interconnection networks for given demands,</li> <li>can use the learned structures to develop new high-performance interconnection networks</li> </ul>			
Contents: • Topologies, Switching, Routing, Flow Control • Fault tolerance and Deadlocks • Collective Communications • Congestion Management • Network Interfaces • On-Chip Networks • Performance Evaluation and Simulation				
Prerequisites:         Recommended Knowledge:           none         MScTI_PCA, MScTI_APC				
Literature: A read The script will be a	ling list will be accessible on t	provided in the script. the web site of the Comp	uter Architecture Gro	oup.
Corres of Tooting	and Evenined	ion 201 min and avam	ar Ob written even	n n n un n n d

**Form of Testing and Examination:** 30<sup>°</sup> min. oral exam or 2h written exam, announced by Module Coordinator

<b>Code:</b> MScTI_ROB1		Course Title: Robotics 1 - Kinematics, Dynamics and Control			
Module Coordi Prof. Dr. L. Mas	<b>nator:</b> ia	<b>Type:</b> Lecture with exercises	5		
<b>Credit Points:</b> 6	Workload: 180h	<b>Teaching Hours:</b> 4 / week	<b>Language:</b> German / English	Term: WS	
Module Parts a <ul> <li>Lecture (2 ho</li> <li>Exercises (2</li> </ul>	<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture (2 hours / week)</li> <li>Exercises (2 hours / week)</li> </ul>				
Objectives: The students					
<ul> <li>can apply print</li> <li>can explain the kinematics are</li> <li>can give an construction of the construction of the system,</li> <li>can understame</li> </ul>	<ul> <li>can apply principles of mechanics to mechanisms and robotics problems,</li> <li>can explain theory and solve problems using appropriate algorithms of robot kinematics and dynamics,</li> <li>can give an overview on state-of-the-art robotics applications in various fields,</li> <li>can explain the function of robotics hardware such as actuators, sensors in a robotic system,</li> </ul>				
Contents:					
<ul> <li>State of the a assistive devi</li> <li>State of the a Space, Trans</li> <li>Actuators and</li> <li>Mechanical c</li> <li>Forward and</li> <li>Differential ki</li> <li>Trajectory ge</li> <li>Motion plann</li> <li>Dynamics</li> <li>Robot contro</li> </ul>	<ul> <li>Contents:</li> <li>State of the art robot types (Humanoid robots, manipulators, wearable robots and assistive devices, swarm robots, unmanned land/sea/aerial vehicles, etc.)</li> <li>State of the art robot applications in (Industry, Medicine, Care, Rescue/Humanitarian, Space, Transport etc.)</li> <li>Actuators and sensors in robotics</li> <li>Mechanical concepts, rigid body motions and homogeneous transformations</li> <li>Forward and Inverse kinematics of open chains</li> <li>Differential kinematics and statics</li> <li>Trajectory generation in joint and cartesina workspace</li> <li>Motion planning</li> <li>Dynamics</li> </ul>				
Prerequisites: none		Recommended Know Basic knowledge in M	<b>wledge:</b> lechanics and Linear	Algebra	
<ul> <li>Literature:</li> <li>B. Siciliano, et al: "Robotics - Modeling, Planning and Control"</li> <li>F. Park &amp; K. Lynch: "Modern Robotics – Mechanics, Planning and Control"</li> <li>Mark W. Spong, Seth Hutchinson and M. Vidyasagar: "Robot Dynamics and Control", second edition</li> </ul>					

Successful participation in the exercises is required to be accepted to exam.

Code:Course Title:MScTI_ROB2Robotics 2 - Simulation and Optimization in Robotic		Course Title: Robotics 2 - Simula	ation and Optimization	in Robotics	
Module Coordinator: Prof. Dr. L. Masia		<b>Type:</b> Lecture with exercise	ses		
Credit Points: Workload	:	Teaching Hours:	Language:	Term:	
6 180h		4 / week	German / English	SS	
Module Parts and Teaching	ng M	ethods:			
<ul> <li>Lecture (2 hours / week)</li> <li>Programming Exercises (2 hours / week)</li> </ul>					
Objectives: The students	•				
<ul> <li>can explain and apply advanced principles of modeling, optimization and control of dynamic processes, in particular mechanical systems,</li> <li>can apply nonlinear optimization and optimal control methods and can compare and evaluate different mathematical approaches,</li> <li>can model, classify and analyze complex motions of mechanical systems, e.g. in robotics or biomechanics, and investigate specific properties such as stability,</li> <li>know how to use software tools based on C++ and Lua for modeling, simulation, optimization and visualization of humanoid and robotic systems,</li> <li>are capable of solving optimal control problems numerically and to evaluate the quality</li> </ul>					
Contents:					
<ul> <li>Dynamic process modeling</li> <li>Modeling of complex mechanical systems (e.g. humanoids)</li> <li>Simulation of mechanical Systems (Integrators and Initial Value Problems)</li> <li>Boundary value problems</li> <li>Nonlinear optimization problems</li> <li>Optimal control problems in robotics</li> <li>Direct and indirect methods for optimal control problems</li> <li>Stability of dynamical systems</li> <li>Simulation and visualization of mechanical systems</li> <li>Modeling multi body systems with RBDL (Rigid Body Dynamics Library)</li> <li>Visualization of motions of mechanical systems with Puppeteer</li> <li>Solution of optimal control problems with MUSCOD-II for different mechanic al examples</li> </ul>					
Prerequisites:		Recommended Kr	nowledge:		
Robotics 1 - Kinematics, Dynamics and Control		Knowledge in Matla	ab/Octave		
or Theoretical Mechanics o similar knowledge	or Theoretical Mechanics or similar knowledge Optimization 1, Numerical mathematics 1;				
Literature:					
<ul> <li>J. T. Betts: "Practical Methods for Optimal Control Using Nonlinear Programming"</li> <li>J. Nocedal, S. Wright: "Numerical Optimization"</li> </ul>					
Form of Testing and Examination: Written exam at the end of the semester.					

Successful participation in the programming exercises is required to be accepted to exam.

Code:		Course Title:			
MScTI_BIOMEC	CH	Biomechanics and Biorobotics			
Module Coordi Prof. Dr. L. Mas	<b>nator:</b> ia	<b>Type:</b> Lecture with exercises			
Credit Points:	Workload:	Teaching Hours:	Language:	Term:	
6	180h	4 / week + block	German / English	WS	
Module Parts a	nd Teaching M	ethods:			
<ul> <li>Lecture (2 ho</li> <li>practical (block)</li> </ul>	ours / week) with ck)	exercises (2 hours / wee	k)		
Objectives: The students					
<ul> <li>can explain the can distinguistic can model different activity, reflexing understand the such as marked in the can explain the can explain the can understate biometric and are able to interpret can write code in the can write code in the can present predia, are able to for the can analyze the can present predia, are able to for the can analyze the can present predia, are able to for the can analyze the can are able to for the can analyze the can present predia, are able to for the can analyze the can analyze the can present predia, are able to for the can analyze the can are able to for the can analyze the can are able to for the can analyze the can are able to for the can analyze the can are able to for the can analyze the can are able to for the can analyze the can are able to for the can are able to</li></ul>	he basics of hum sh between differ (ferent aspects of kes), he function and a ker-based and IN he concept of hu nd the theory be d biomechanical dependently play motion capture d le for analysis or project results in	nan physiology, rent concepts of biologica of biological motion gener are familiar with the use of 1U based motion capture man machine interaction hind control of interacting signals n and execute a biomech lata with respect to a spe visualization of biomech a scientific way using po	al motion, ration (neural control of devices for motion systems and electro and biorobotics g system for measuri nanical study, possib cific biomechanical of anical data, sters, presentations	, muscle analysis omyography, ing human ly in a team, question, or other code.	
Contents:					
<ul> <li>Physiological basics of the human body and of animals</li> <li>Body proportions and anthropometric data</li> <li>Muscle physiology and muscle models</li> <li>Neural control of biological motion and interaction</li> <li>Human sensor systems and sensor-based motion control</li> <li>Human motion/interaction measurements: camera and marker based (sparse) motion capture, IMU based motion capture, electromyography, force plates, pressure soles, markerless motion capture</li> <li>Biorobotics and human-robot interaction</li> <li>Control of interactive robotic devices</li> <li>Methodological principles of control and experimental design using robotics</li> <li>Human motion and interaction performance analysis</li> </ul>					
Prerequisites:		Recommended Knowl	ledge:	tral	
none			, bynamics and Con	uoi	
<ul> <li>Literature:</li> <li>Robert McN</li> <li>David A. Win</li> <li>Etienne Burg</li> </ul>	eill Alexander: "E hter: "Biomechar det , David W. Fra	Exploring Biomechanics - nics and Motor Control of anklin, e al. Human Robo	Animals in Motion" Human Movement" tics: "Neuromechanic	cs and Motor	

Control"
Reza Shadmehr, Steven P. Wise: "The Computational Neurobiology of Reaching and Pointing: A Foundation for Motor Learning" (Computational Neuroscience Series)  Reza Shadmehr, Sandro Mussa-Ivaldi: "Biological Learning and Control: How the Brain Builds Representations, Predicts Events, and Makes Decisions" (Computational Neuroscience Series)

**Form of Testing and Examination:** Successful completion of biomechanical lab project with presentation and report

Code: MScTI_HAPTIC	S	Course Title: Haptics and Human Robot Interaction / Rehabilitation		
<b>Module Coordinator:</b> Prof. Dr. L. Masia		<b>Type:</b> Lecture with exercises		
<b>Credit Points:</b>	<b>Workload:</b> 180h	<b>Teaching Hours:</b> 4 / week	<b>Language:</b> German / English	Term: SS
<ul> <li>Module Parts and Teaching Methods:</li> <li>Lecture (2 hours / week)</li> <li>Programming Exercises (2 hours / week)</li> </ul>				
<ul> <li>Objectives: The</li> <li>can understa</li> <li>can run CAD</li> <li>know the diffe</li> <li>can explain a mechanical s</li> <li>can apply con</li> <li>can model, a investigate st</li> <li>know how to and data visu</li> <li>know how to</li> <li>are capable o statistical ana</li> </ul>	e students nd the design pr program and de erent technologie and apply princip systems, ntrol methods for ctuators and me tability robustnes use software too alization in reha implement a sta of analyzing data alysis.	inciples behind assistive esign basic interactive sys cal solutions for haptics a les of modeling and cont r human-robot interaction chanical systems, in robo is and metrological perfor ols based on Matlab Simu bilitation devices, ble controller for haptic, a collected by means of re	technology, stems, and robotic rehabilita rol of dynamically int n devices, ptics or biomechanic rmance, ulink for modeling, si ehabilitation devices	tion, eracting s, and mulation, and running
statistical analysis.         Contents:         • Dynamically interacting mechanical systems (e.g. haptic devices )         • Sensing and motor specialization in human physiology         • Haptics and human robot interaction         • Actuation, sensors and controllers for haptics         • Mechanical design solutions of interacting Robots         • End Effector robots, exoskeletons and exosuits         • Introduction to CAD for mechanical systems and haptic devices         • Control problems in rehabilitation robotics         • Admittance and impedance controllers         • Stability of dynamically interacting systems         • Foundation of prosthetics and orthotics         • Mechanical measurement for human machine interactions         • Clinical data analysis and statistics         Prerequisites:         none				
• Thorsten A	(ern Engineerin	a "Hantic Devices: A Bec	ninner's Guide for En	aineers"

- Thorsten A. Kern. Engineering "Haptic Devices: A Beginner's Guide for Engineers"
  Ming C. Lin e Miguel Otaduy. "Haptic Rendering: Foundations, Algorithms, and Applications" (English Edition)"
- Lynette Jones. "Haptics" (MIT Press Essential Knowledge series)
- Material provided by the Instructor and Module Coordinator

**Form of Testing and Examination:** Successful completion of working groups lab project, using available setup with final presentation and report.

Code:		Course Title:		
MScTI_ROGA		Robotic Games		
Module Coordina	Module Coordinator:			
Prof. Dr. L. Masia		Project oriented cours	e	
Credit Points:	Workload:	Teaching Hours:	Language:	Term:
6	180h	4 / week	German / English	WS/SS
Module Parts and	Teaching M	ethods:		
Supervised	l project group	os in the Laboratory		
Theoretica	l foundations i	ns in lectures form		
Objectives: After	this course the	e students will be able to	o:	
<ul> <li>design an a</li> </ul>	autonomous n	is mobile robot		
solve a two-player non-cooperative game and implement the solution in real-time				in real-time
Contents:				
Behavioral	-based recurs	ive, nested control struc	ture (RNBC)	
<ul> <li>Kinematics</li> </ul>	of wheeled m	nobile robots		
Non-holon	omic control			
<ul> <li>Foundation Cat and Mo</li> </ul>	ns of game the ouse	ory in particular non-co	operative two-player g	james, e.g.
<ul> <li>Practical in algorithms</li> </ul>	nplementation used	s and experimental test	ing of the methods an	d
Prerequisites:		Recommended Know	vledge:	
none		Knowledge of C/C++		
Literature:				
• Badreddin, E.:	"Control and	System Design of Whee	eled Mobile Robots",	
Habilitationssc	hrift, 1997 kin M÷"Com	nutational Principles of	Mohile Robotics" Car	nhridae
University Pres	ss, 2000			libridge
• Y. C. Shin and C. Xu: "Intelligent Systems: Modeling, Optimization, and Control",				
J. Engwerda: "	LQ Dynamic (	Optimization and Diffe	rential Games", J. W	/iley, 2005
Form of Testing a	and Examinat	ion:		

Experimental demonstration, oral presentation and short written report

Code: MScTI_ROBP		Course Title: Robotics Practical		
Module Coordi Prof. Dr. L. Masi	<b>nator:</b> ia	<b>Type:</b> Practical course		
<b>Credit Points:</b>	Workload: 180h	<b>Teaching Hours:</b> 4 / week	<b>Language:</b> German / English	Term: WS/SS
<ul><li>Module Parts and Teaching Methods:</li><li>Practical course in groups of 2 persons</li></ul>				
<ul> <li>Objectives: The</li> <li>can use different and challenge</li> <li>are able to in</li> <li>can apply the</li> <li>can present research and r</li></ul>	e students rent types of rob es related to the dependently plan coretical knowled results of a robot	otics hardware and ex m, n and execute robotics lge in robotics to imple ics project in a scientil	plain the respective ap s projects in a team, ement solutions on rea fic way.	oplications I platforms,
<ul> <li>Contents:</li> <li>Challenges of real hardware vs. model calculations</li> <li>Working principles and practical implementation of sensors and actuators</li> <li>Development or modification of robotic hardware</li> <li>Code development for specific hardware</li> <li>Robotic projects on different kinds of hardware such as (but not limited to) <ul> <li>Robot arm</li> <li>Mobile platform</li> <li>Unmanned aerial vehicle</li> </ul> </li> </ul>				
Prerequisites: none	Prerequisites:       Recommended Knowledge:         none       Basic knowledge in C/C++         Robotics 1 - Kinomatics, Dynamics and Control			
Literature: • Script				
Form of Testing	g and Examinat	ion: Oral colloquium a	and written documenta	ition.

Code: MScTI_SEM		Course Title: Advanced Seminar		
Module Coordina all groups	tor:	<b>Type:</b> Seminar with presentation		
<b>Credit Points:</b> 4 + 2 (soft skills)	<b>Workload:</b> 180h	<b>Teaching Hours:</b> 2 / week	Language: German / English	Term: WS/SS
Module Parts and • Seminar	I Teaching M	ethods:		
<ul> <li>search liter</li> <li>select subj</li> <li>prepare ma</li> <li>give a sciel</li> </ul>	ature for a spo ect / material f aterial (slides) ntific presenta	ecific subject, for a presentation, for a presentation, tion.		
Contents: Literature r Preparation Oral Prese Preparation Active part	esearch n of presentati ntation (~45 M n of a short su icipation in oth	on 1inutes) mmary report (~10 pa ner student's presenta	iges) tions & discussion	
Prerequisites: none	requisites:Recommended Knowledge:eGeneral knowledge about the chosen field			
Literature: Partially provided I	oy Module Co	ordinator		
Form of Testing a participation	and Examinat	tion: Presentation, wr	itten summary, regula	r active

Code: MScTI_SA		Course Title: Student Research Project		
Module Coordi all groups	nator:	<b>Type:</b> Practice course		
<b>Credit Points:</b> 14	Workload: 420h	Teaching Hours:Language:Term:n.a.German / EnglishWS / SS		
Module Parts a • Practical	ind Teaching M I course	ethods:		
<ul> <li>Objectives: Aft</li> <li>dig into s</li> <li>manage</li> <li>write a n</li> </ul>	<ul> <li>Objectives: After this course the students will be able to:</li> <li>dig into scientific and technical aspects of a selected topic,</li> <li>manage and carry through a small research project,</li> <li>write a medium length report.</li> </ul>			
• Researc • Manage • Preparat	<ul> <li>Contents:</li> <li>Research work on a specific topic.</li> <li>Management of work.</li> <li>Preparation of a medium length report.</li> </ul>			
Prerequisites: none	Prerequisites:     Recommended Knowledge:       none     Knowledge in research field			
Literature: Depending on subject, provided by supervisor				
Form of Testin	g and Examina	t <b>ion</b> : Written report		

Code: MScTI_THESIS		Course Title: Master Thesis		
Module Coordina all groups	itor:	<b>Type:</b> Practice course		
<b>Credit Points:</b> 30	<b>Workload:</b> 900h	Teaching Hours: n.a.	Language: German / English	Term: WS / SS
Module Parts and Teaching Methods:     Master Thesis				
<ul> <li>manage ar</li> <li>write an ex</li> <li>report on o</li> </ul> Contents: <ul> <li>Research v</li> <li>Manageme</li> <li>Preparation</li> </ul>	work on a specent of work.	york in an oral presenta cific topic.	o. ect, tion.	
Prerequisites:	• Oral presentation in the conoquium.       Prerequisites:     Recommended Knowledge:       none     Knowledge in research field			
Literature: Depending on sub	ject, provided	by supervisor	loquium	
			οφαιαπ	